

### ABSTRACT OF THE DISCLOSURE

An integrated circuit carrier includes a wafer having a receiving zone. The receiving zone is demarcated by a bore in the wafer. A plurality of island-defining portions is arranged about the receiving zone. Each island-defining portion has an electrical terminal electrically connected to an electrical contact of said at least one receiving zone. A rigidity-reducing arrangement connects each island-defining portion to each of its neighboring island-defining portions.

Figure 11